

A Printed Pages : 3
(21119) Roll No.
B.C.A.-III Sem.

18013

B.C.A. Examination, November-2019
COMPUTER ARCHITECTURE AND
ASSEMBLY LANGUAGE
(BCA-303)

Time : Three Hours [Maximum Marks : 75

Note : Attempt questions from *all* sections as per instructions.

Section-A

(Very Short Answer Questions)

Note : Attempt all *five* questions. Each question carries 3 marks. Very short answer is required not exceeding 75 words. $5 \times 3 = 15$

1. What is the role of Assemblers ?
2. What are Macros ?
3. Define the Computer Registers.
4. Write short note on asynchronous data transfer.
5. What is cache memory ? Describe its operations in brief.

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[P.T.O.]

Section-B

(Short Answer Questions)

Answer any *two* questions out of the following three questions. Each question carries $7\frac{1}{2}$ marks. Short answer is required not exceeding 200 words.

$2 \times 7\frac{1}{2} = 15$

6. Define interrupt. Explain its various types.
7. Differentiate between direct and indirect addressing with an example.
8. Explain subroutine in assembly language.

Section-C

(Detailed Answer Questions)

Attempt any *three* questions out of the following five questions. Each question carries 15 marks. Answer is required in detail. $3 \times 15 = 45$

9. What is Booth algorithm ? Explain it in detail. Multiply 24 and -7 using Booth algorithm.
10. Write short note on following :
 - (i) Synchronous Data Transfer
 - (ii) Serial Communication

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(iii) Index Register

11. Describe the concept of DMA used in computer organisation.
12. (a) A CISC chip is a complex instruction set computing chip the alternative to RISC chips. How do they differ ?
(b) Define Instruction Cycle. Explain each phase.
13. Explain the difference between hardwired control and microprogrammed control. Is it possible to have a hardwired control associated with a control memory.

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