

COMPUTER ARCHITECTURE AND ASSEMBLY**Language (BCA-303) (New)****Time : Three Hours]****(Maximum Marks : 75****Note :** Attempt all sections as per instructions.**Section-A****(Very Short Answer Questions)****Note :** Attempt all five questions. Each question carries equal marks. Very short answer is required not exceeding 75 words. 5x3=15

1. What is the use of register? Name any two registers.
2. What do you mean by cycle stealing?
3. Differentiate between hardwired and micro-programmed controls.
4. Explain in brief, how subroutine call is different from a software interrupt?
5. Discuss the stack organization for processor.

Section-B**(Short Answer Questions)****Note :** Attempt any two questions. Each question carries equal marks. Short Answers is required not exceeding 200 words. 2x7.5

1. What are the steps required for a pipelined processor to process the instruction? Draw a pipeline for (A+B*D).
2. Write a program in assembly to subtract number stored at memory location 2001H from the number stored at 2002H. Display the result at 2004H.

OR

What is Booth algorithm? Explain its advantages.

3. What is asynchronous data transfer? How! handshaking removes the disadvantage of strobe method?

Section-C**(Detailed Answer Questions)****Note :** Attempt any **three** questions. Each question carries equal marks. Answer is required in detail.3x15=45

1. Draw and explain the architecture of 8085 Microprocessor along with all its registers and instruction set.
2. What do you mean by addressing modes? Explain various addressing modes with the help of examples.
3. What do you mean by Input-output Processor (IOP)? Explain with the help of block diagram
4. What do you understand by Instruction Cycle? What are the different phases of instruction cycle?
5. Write short note on:

(a) RISC/CISC (b) Vector Processor (c) Array Processor (d) Parallel Processor (e) DMA

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